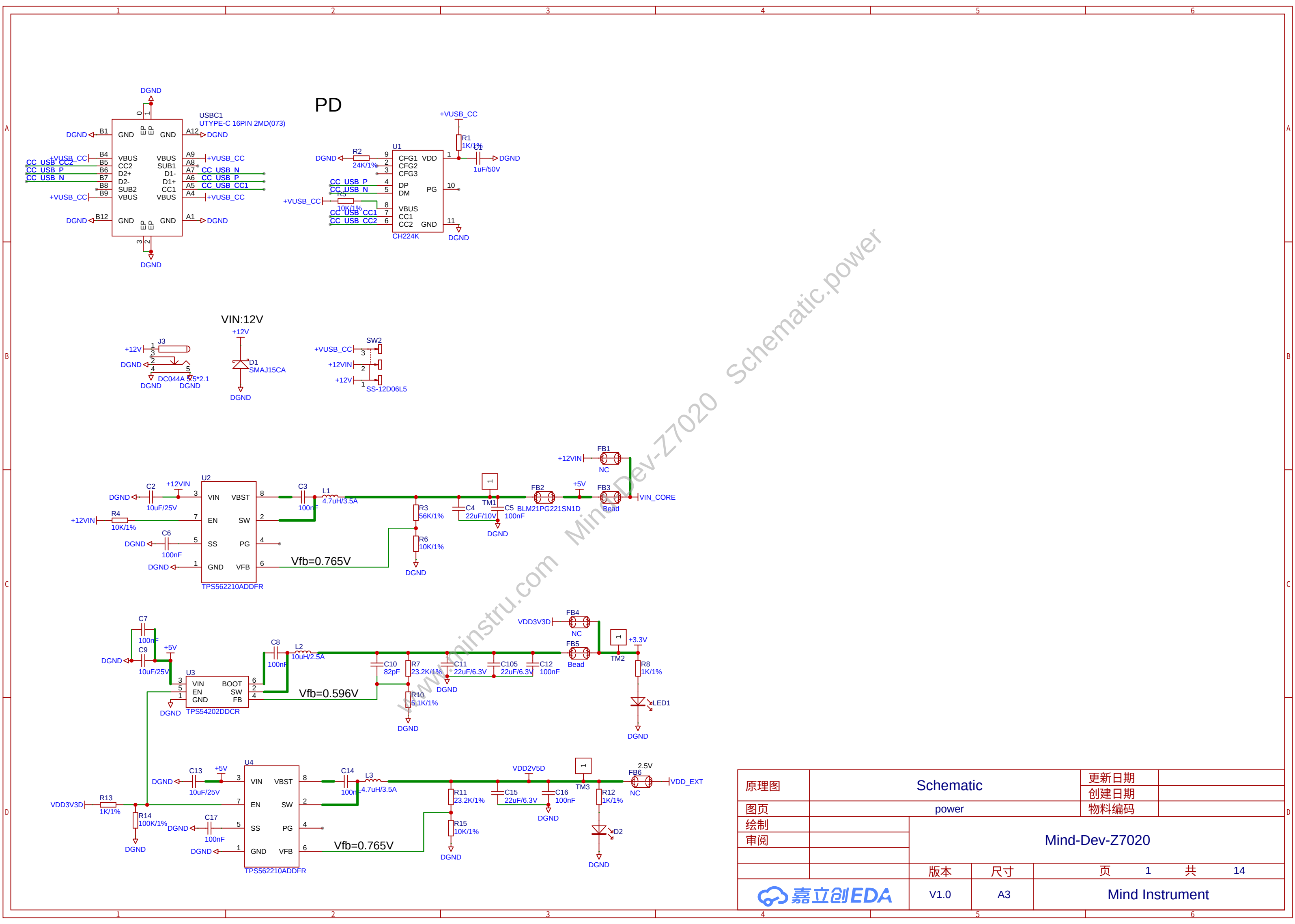
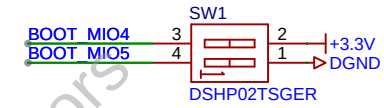
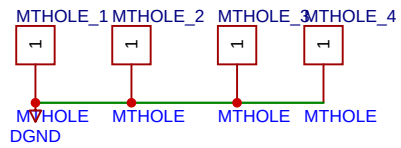
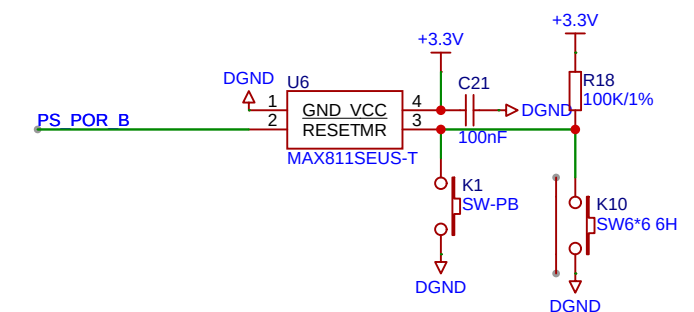
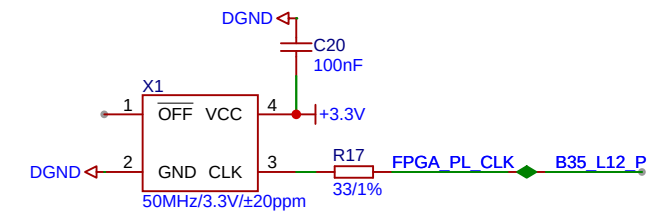
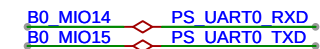
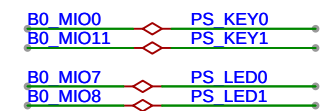
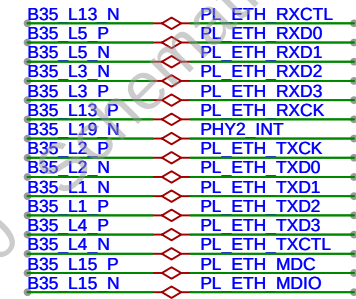
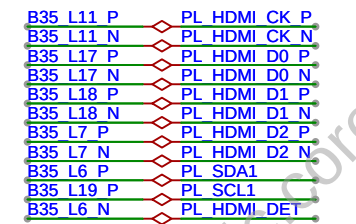




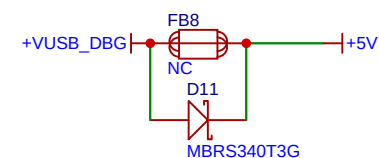
原理图	Schematic		更新日期	
			创建日期	
图页	power		物料编码	
绘制	Mind-Dev-Z7020			
审阅				
	版本	尺寸	页 1 共 14	
嘉立创EDA		V1.0	A3	Mind Instrument

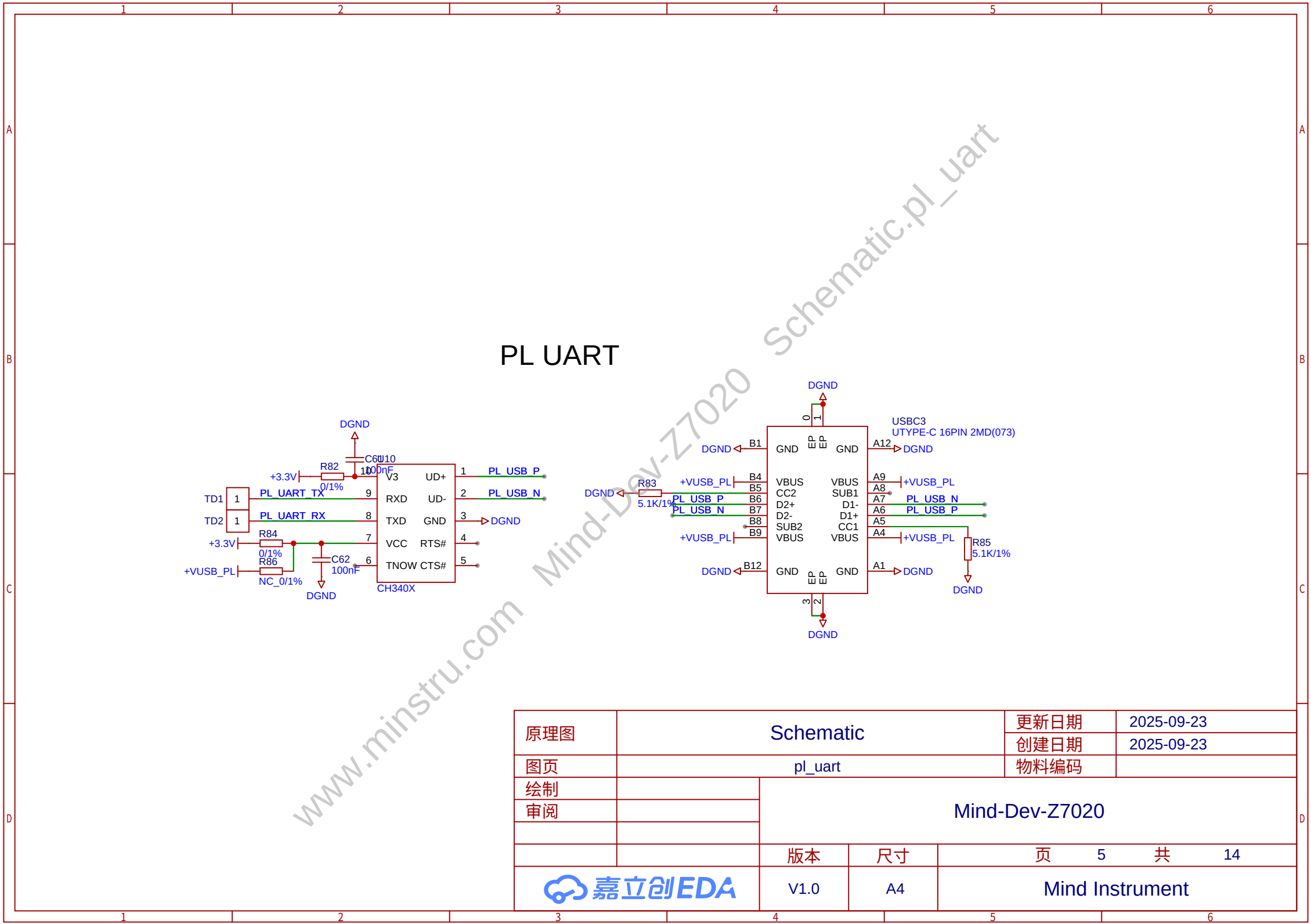


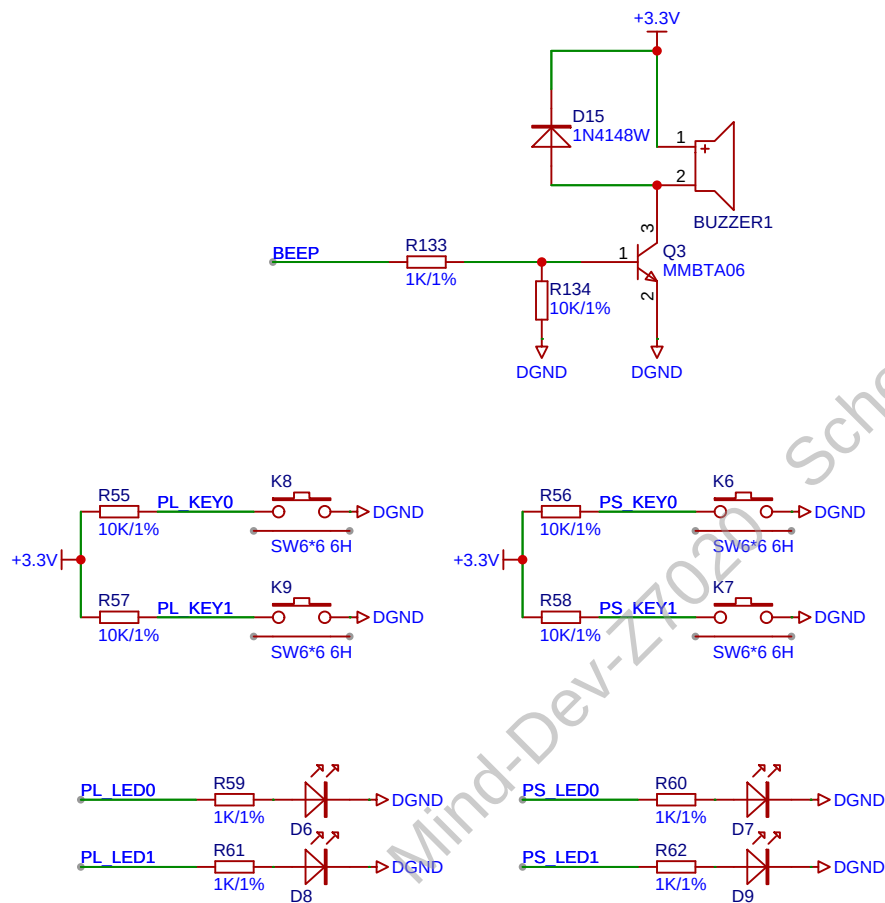
PLL mode: PLL Used
MIO0 Voltage: 2.5V/3.3V
MIO1 Voltage: 1.8V



原理图	Schematic		更新日期	
图页	core_connectors		创建日期	
绘制		Mind-Dev-Z7020		
审阅				
		版本	尺寸	页 2 共 14
		V1.0	A3	Mind Instrument

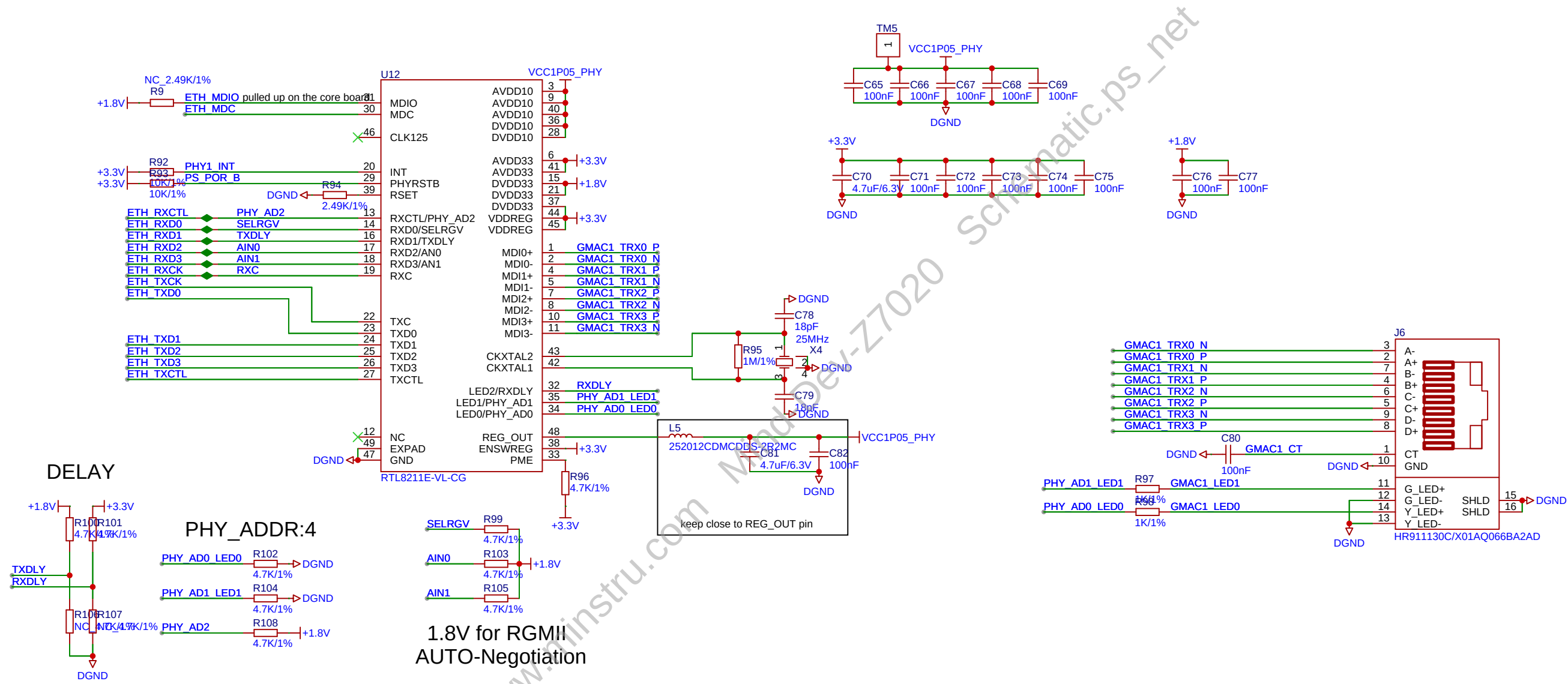






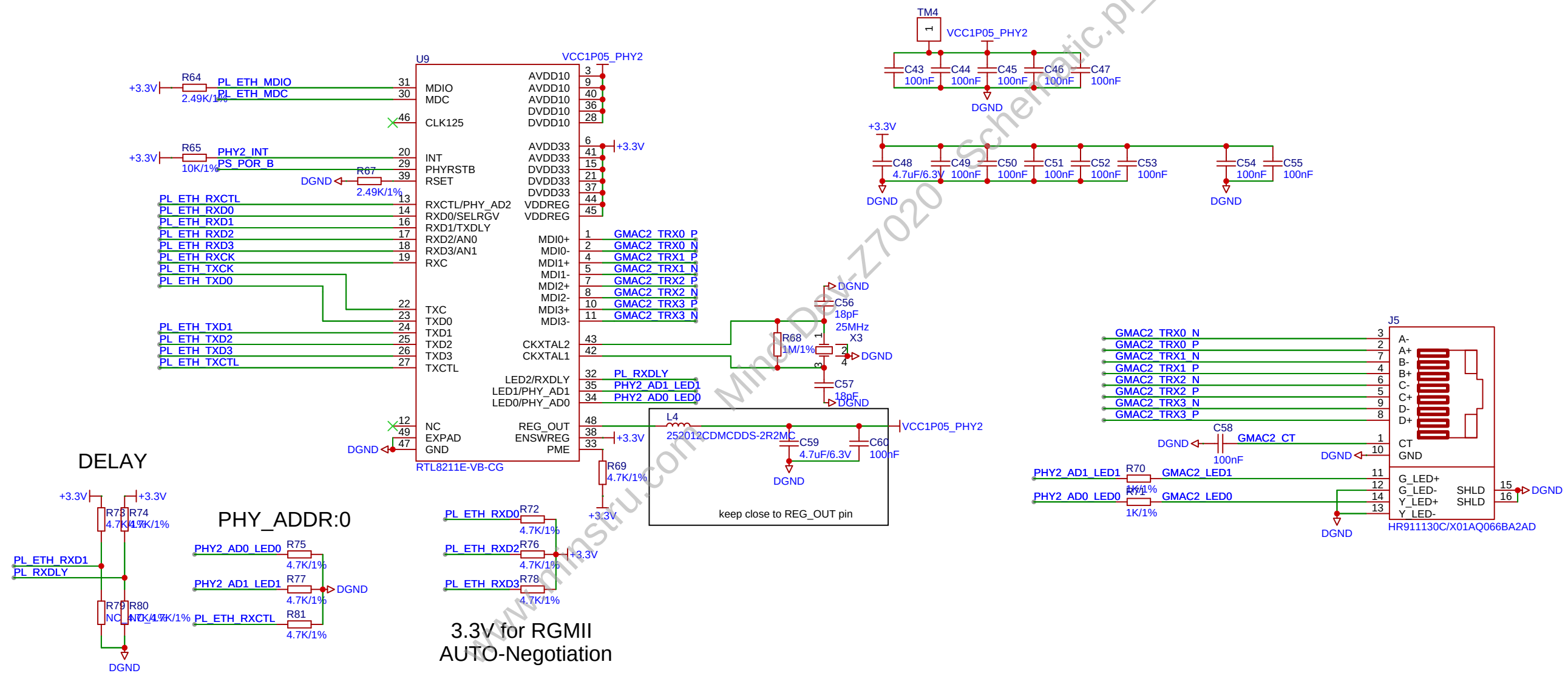
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图页	led_key			物料编码	
绘制		Mind-Dev-Z7020			
审阅					
		版本	尺寸	页 6 共 14	
嘉立创EDA		V1.0	A4	Mind Instrument	

PS LAN



原理图	Schematic		更新日期	
图页	ps_net		创建日期	
绘制		Mind-Dev-Z7020		
审阅				
		版本	尺寸	页 7 共 14
嘉立创EDA		V1.0	A3	Mind Instrument

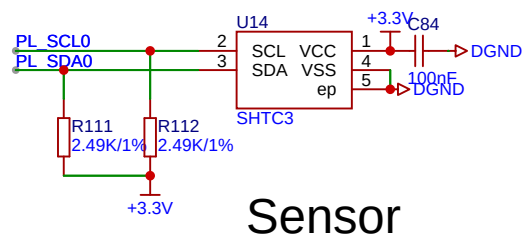
PL LAN



原理图	Schematic			更新日期	
				创建日期	
图页	pl_net			物料编码	
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审阅					
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		V1.0	A3	Mind Instrument	

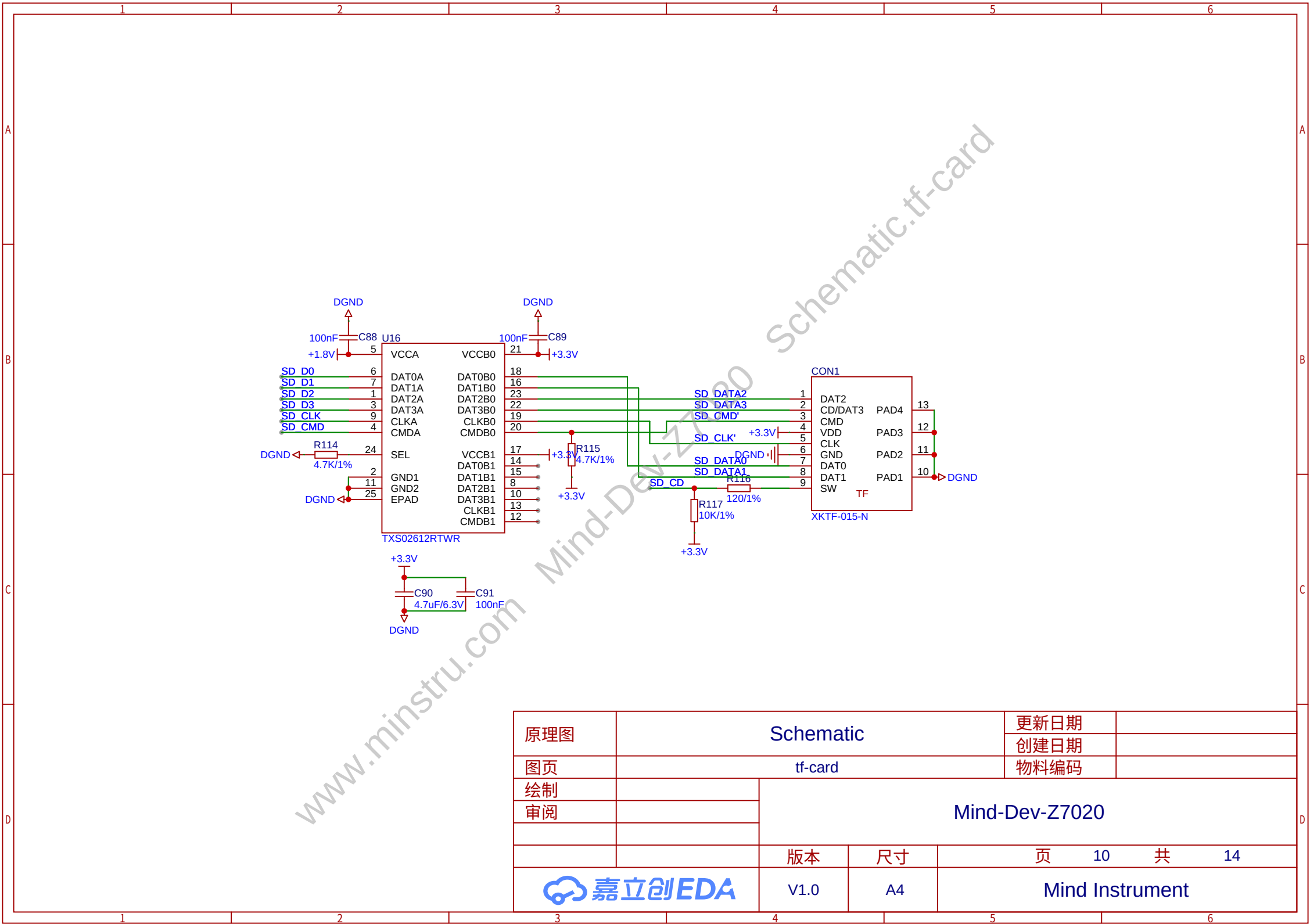
Diagram illustrating the AT24LC64M/TR memory chip connected to a +3.3V supply and I2C bus lines (SDA, SCL).

- Chip:** AT24LC64M/TR
- Pin Connections:**
 - Pin 1: A0
 - Pin 2: A1
 - Pin 3: A2
 - Pin 4: GND
 - Pin 5: SDA
 - Pin 6: SCL
 - Pin 7: WP
 - Pin 8: VCC
- External Components:**
 - Capacitor C83 (100nF) connected to VCC (Pin 8) and GND.
 - Resistor R109 (10k/100k) connected to VCC (Pin 8) and SDA (Pin 5).
 - Resistor R110 (0/1%) connected to SDA (Pin 5) and SCL (Pin 6).

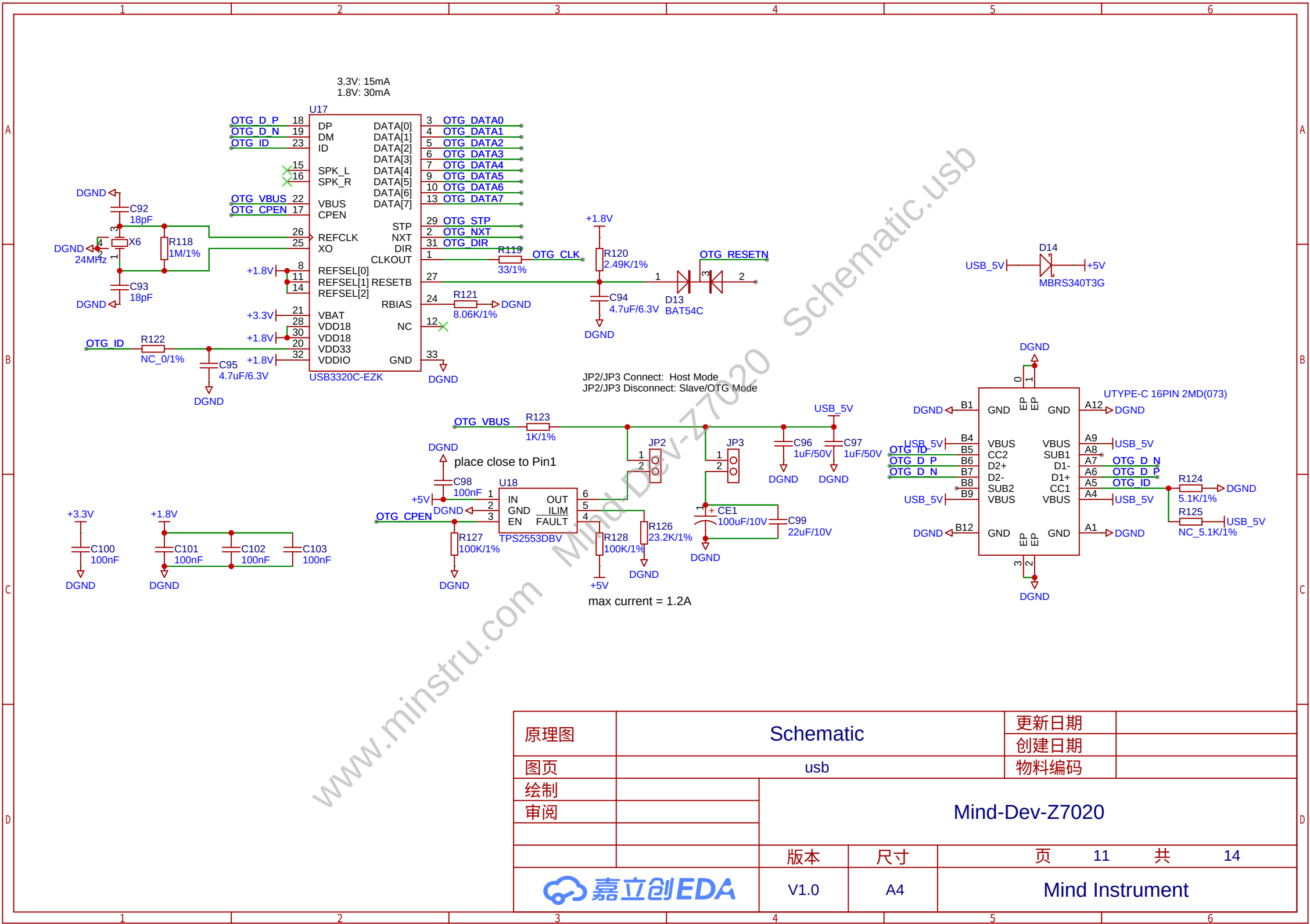


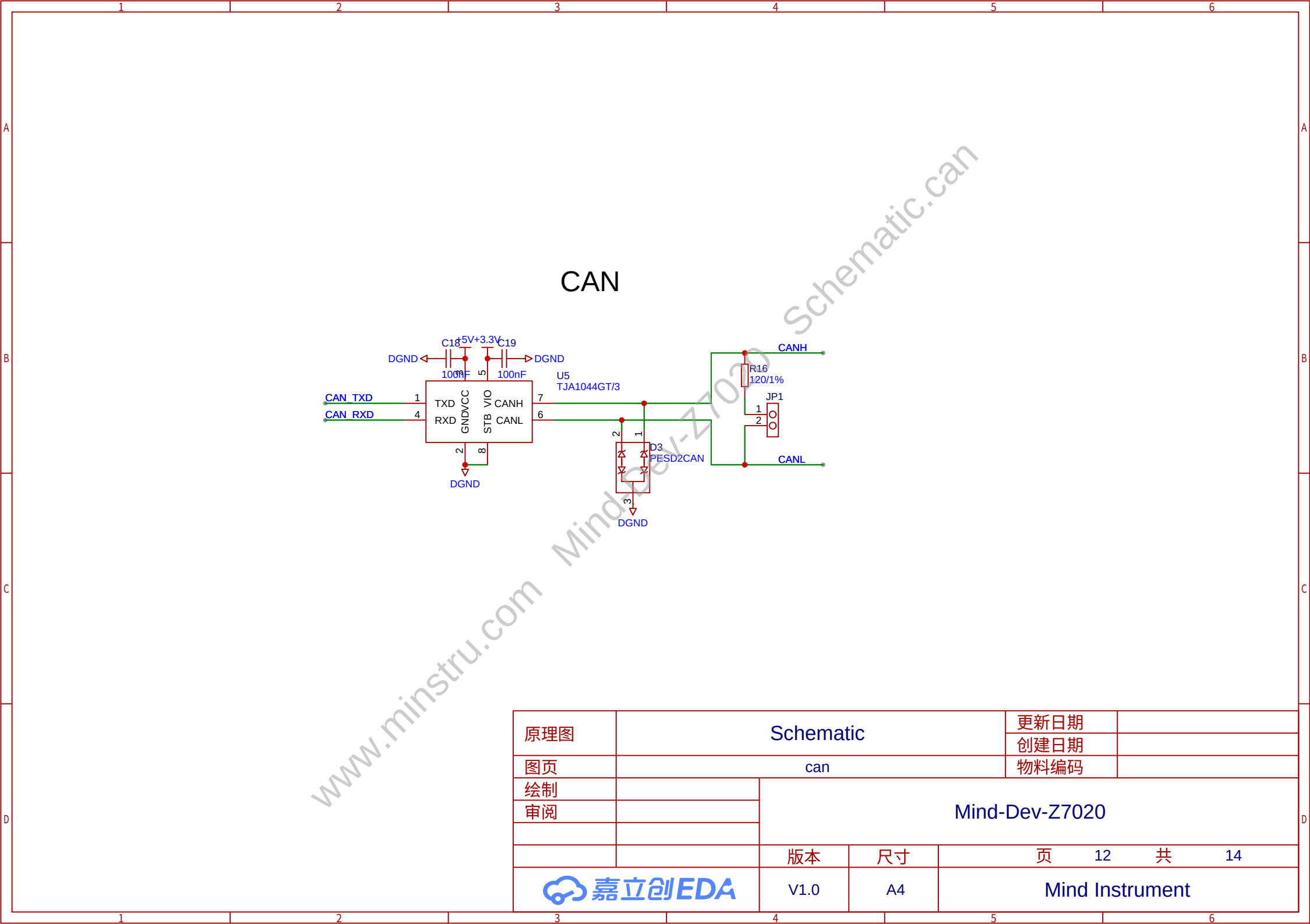
The schematic diagram shows the PCF8563T module (U15) connected to various components. The XTAL32 and EXTAL32 pins are connected to a 32.768KHz crystal (X5) and a 1MΩ resistor (R113) to ground. The VCC_RTC pin is connected to a 100nF capacitor (C86) to ground. The XDCR-1220-006 pin is connected to a +3.3V supply through a diode (BAT54C).

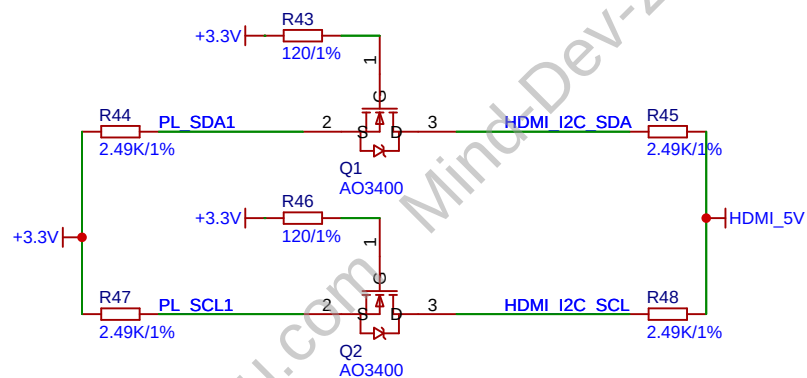
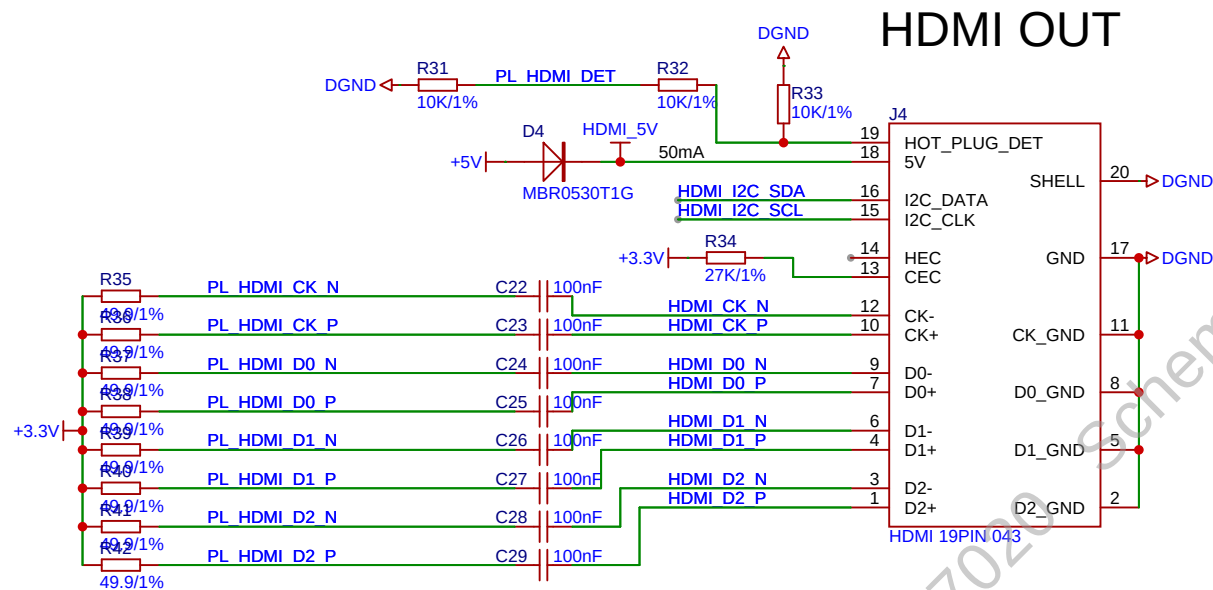
原理图	Schematic			更新日期	
图页	sensor_e2prom_rtc			创建日期	
绘制		Mind-Dev-Z7020			
审阅					
		版本	尺寸	页	9 共 14
		V1.0	A4	Mind Instrument	



原理图	Schematic		更新日期	
图页	tf-card		创建日期	
绘制		Mind-Dev-Z7020		
审阅				
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嘉立创EDA		V1.0	A4	Mind Instrument

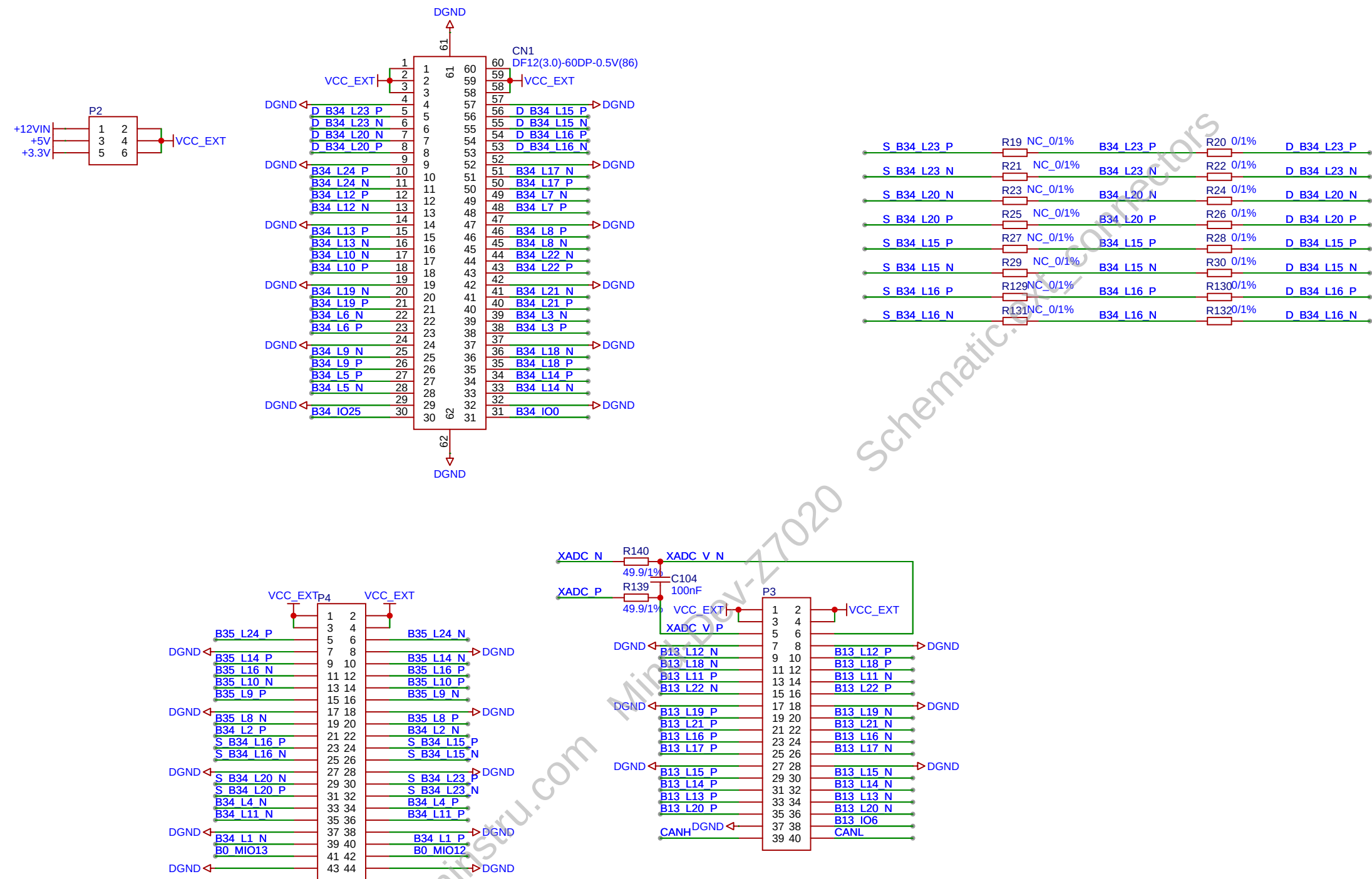






原理图	Schematic		更新日期	
图页	hdmi		创建日期	
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审阅				
	版本	尺寸	页 13	共 14
嘉立创EDA		V1.0	A4	Mind Instrument

Extension Connectors



原理图	Schematic			更新日期	
				创建日期	
图页	ext_connectors			物料编码	
绘制		Mind-Dev-Z7020			
审阅					
		版本	尺寸	页	14 共 14
嘉立创EDA		V1.0	A3	Mind Instrument	